

**S/N 10/642,702**

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Vivek Jaiswal et al.	Examiner:	Chirag R. Patel
Serial No.:	10/642,702	Group Art Unit:	2141
Filed:	August 18, 2003	Docket No.:	P16507
Title:	DYNAMIC LOAD DISTRIBUTION WITHIN A SESSION INITIATION PROTOCOL NETWORK		
Assignee:	Intel Corporation	Confirmation No.:	4365

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**RESPONSE AFTER FINAL REJECTION**

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

This is in response to the Final Office Action mailed March 18, 2009. Reconsideration and allowance of the subject application, in light of the following Remarks, are respectfully requested.